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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/801,625	03/08/2001	Adolphe Johannes Gerardus Ruigt	NL 000095	8317	
24737	2590 05/06/2003				
PHILIPS ELECTRONICS NORTH AMERICAN CORP			EXAMINER		
	580 WHITE PLAINS RD TARRYTOWN, NY 10591			KOVALICK, VINCENT E	
	•		ART UNIT	PAPER NUMBER	
			2673	₹	
			DATE MAILED: 05/06/2003	<i>-</i>	

Please find below and/or attached an Office communication concerning this application or proceeding.

1

Application No. Office Action Summary Application No. Og/801,625 Examiner Vincent E Kovalick 2673 Period for Reply A HORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply A HORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE of FIHS COMMUNICATION. Statescens of time rays to available used in the provisions of 37 CFR 1-3869. In no event, however, may a raply be sinsely filled selected to make the State (Month) This from the mining date of this communication, and the state of the provision of the provision of 37 CFR 1-3869. In one event, however, may a raply be investy filled selected for the provision of 37 CFR 1-3869. In one event, however, may a raply be investy filled selected for the provision of 37 CFR 1-3869. In one event, however, may a raply be investy filled selected for the provision of 37 CFR 1-3869. In one event, however, may a raply be investy filled selected period for raply with place state carried period for raply with place state carried period for raply with place state of the splication is to condition. In the period of the communication of the period of the perio								
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	Attachment	(s)						
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	2) Notice	of Draftsperson's Patent Drawing Review (PTO-948)	5) Notice of Informal F					

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DETAILED ACTION

Response to Amendment

1. This Office Action is in response to Applicant's Amendment dated March 11, 2003 in response to PTO Office Action dated November 29, 2002.

The amendments to claims 1-6 and the addition of new claims 7-19 have been noted and entered in the record.

The amendments to claims 1-6 render Applicant's remarks relative to claims 1-6 moot.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1, 7, 13-14 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki et al. (USP 5,500,538) taken with Chiba et al. (USP 5,589,960) in view of Koyama et al. (USP 6,310,598).

Relative to claims 1, 7, 13-14 and 19, Yamazaki et al. **teaches** an electro-optical device and method of driving the same (col. 3, lines 15-56); Yamazaki et al. further **teaches** a liquid crystal display (LCD) device comprising a first substrate provided with one or more first electrodes and a second substrate provide with one or more second electrodes, and a twisted nematic liquid

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crystal material between the two substrates, in which viewed perpendicularly to the substrates, overlapping parts of the electrodes define pixels (col. 16, lines 18-40 and Fig. 20).

Yamazaki et al. **does not teach** the display device being provided with means for adjusting an operating voltage of the liquid crystal display device based on one or more measurements involving a measuring element positioned between the first and second substrates.

Chiba et al. **teaches** a liquid crystal display system (col. 2, lines 60-67 and col. 3, lines 1-25);
Chiba et al. further **teaches** the display device being provided with means for adjusting an operating voltage of the liquid crystal display device based on one or more measurements involving a measuring element (col. 1, lines 1-19; col. 3, lines 62-67 and col. 4, lines 1-19).

Yamazaki et al. taken with Chiba et al. **does not teach** a measuring element positioned between the first and second substrates.

It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the device as taught by Yamazaki et al. the feature as taught by Chiba et al. in order to put in place the feature whereby the operating voltage can be changed as the physical parameters of the liquid crystal change in order to optimize the quality of the displayed image. Koyama et al. **teaches** a matrix type liquid crystal display unit (col. 2, lines 63-67; col. 3, lines 1-67 and col. 4, lines 1-65); Koyama et al. further **teaches** a measuring element positioned between the first and second substrates (col. 1, lines 64-67 and col. 2, lines 1-4). It being understood the control logic being placed on the substrate as taught by Chiba et al. would include the measuring element.

It would have been obvious to a person of ordinary skill in the art at the time of the invention to

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incorporate in the device as taught by Yamazaki et al. taken with Chiba et al. the feature as taught by Koyama et al. in order place the measuring element in as close proximity to the liquid crystal material as possible, keeping the measuring element in the same environment as the liquid crystal.

4. Claims 2, 4, 8, 10, 15 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki et al. taken with Chiba et al. in view of Koyama et al. as applied to claims 1, 7 and 14 in item 3 hereinabove, and further in view of Black (USP 6,412,977).

Relative to claims 2, 4, 8, 10, 15 and 17, Yamazaki et al taken with Chiba et al. in view of Koyama et al. **does not teach** said LCD characterized in that the means for adjusting the operation voltage of the display device comprises means for measuring a current through the measuring element.

Black et al. **teaches** a method for measuring temperature with an integrated circuit device (col. 6, lines 11-67 and col. 7, lines 1-63); Black et al. further **teaches** said LCD characterized in that the means for adjusting the operation voltage of the display device comprises means for measuring a current through the measuring element (col. 10, lines 28-39).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the device as taught by Yamazaki et al. taken with Chiba et al. in view of Koyama et al. the feature as taught by Black et al. in order to put in place a measuring device from which a characteristic of the liquid crystal could be measured in order to adjust the operating voltage of the liquid crystal material to maintain a quality displayed image.

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Regarding claim 4, it would have been obvious to a person of ordinary skill in the at the time of the invention that the means to measure the current through the measuring element as taught by Black et al. could readily be augmented to include the added feature of detecting a peak current.

5. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki et al. taken with Chiba et al. in view of Koyama et al. as applied to claims 1, 7 and 14 in item 3 hereinabove, and further in view of Hodemaekers (USP 4,298,866).

Relative to claim 5, Yamazaki et al. taken with Chiba et al. in view of Koyama et al. does not teach the LCD device wherein the means for adjusting the operating voltage of the display device comprise means for measuring a capacitance of the measuring element.

Hodemaekers **teaches** a LCD device having capacitance compensation (col. 1, lines 5-68; col. 2, lines 1-68; col. 3, lines 1-68 and col. 4, lines 1-46); Hodemaekers further **teaches** the LCD device wherein the means for adjusting the operating voltage of the display device comprise means for measuring a capacitance of the measuring element (col. 5, lines 58-65). It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the device as taught by Yamazaki et al. taken with Chiba et al. in view of Koyama et al. the feature as taught by Hodemaekers in order to put in place a measuring device from which a characteristic of the liquid crystal could be measured in order to adjust the operating voltage of the liquid crystal material to maintain a quality displayed image.

6. Claims 6, 11 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki et al. taken with Chiba et al. in view of Koyama et al. as applied to claims 1, 7 and 14 in item 3 hereinabove, and further in view of Okabe (USP 5,940,184).

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Regading claims 6, 11 and 18, Yamazaki et al. taken with Chiba et al. in view of Koyama et al. does not teach said LCD characterized in that the measuring element comprises a portion of the liquid distal material.

Okabe **teaches** a method and apparatus using a photoconductive layer formed on an electrode and a liquid crystal polymer composite (col. 1, lines 7-67; col. 2, lines 1-67 and col. 3, lines 1-57); Okabe further **teaches** the LCD device wherein the measuring element comprises a portion of the liquid crystal material (col. 9, line 67 and col. 10, lines 1-9).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the device as taught by Yamazaki et al. taken with Chiba et al. in view of Koyama et al. the feature as taught by Okabe in order to get a measurement directly from the liquid crystal material in the display device.

7. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki et al. taken with Chiba et al. in view of Koyama et al. as applied to claim 7 in item 3 hereinabove, and further in view of Oh (USP 6,466,204).

Regarding claim 12, Yamazaki et al. taken with Chiba et al. in view of Koyama et al. does not teach said LCD further comprising a power supply operable to provide the operating voltage.

Oh teaches a color LCD interface circuit (col. 1, lines 30-58); Oh further teaches said LCD further comprising a power supply operable to provide the operating voltage (col. 2, lines 16-19). It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the LCD unit a power supply operable to provide the operating voltage in that the feature of power supplies and power regulators being incorporated in display devices is well known and in common practice in the art.

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Allowable Subject Matter

8. Claims 3, 9 and 16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Regarding claim 3, the major difference between the teaching of the prior art of record (Yamazaki et al. (USP 5,500,538); Chiba et al. (USP 5,589,960) and Koyama et al. (USP 6,310,598)) and that of the instant invention is that said prior art of record **does not teach** a LCD device characterized in that the means for adjusting the operating voltage of the display device comprises means for raising the operating voltage and simultaneously measuring the current through the measuring element.

Relative to claims 9 and 16, , the major difference between the teaching of the said prior art of record and that of the instant invention is that said prior art of record **does not teach** said LCD wherein the controller is operable to adjust the operating voltage of the LCD device such that a transmission strength of the pixels is fifty percent of a maximum transmission strength.

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Conclusion

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

U. S. Patent No. 6,411,272 Edwards

U. S. Patent No. 5,850,205 Blouin

U. S. Patent No. 5,726,727 Shibahara et al.

10. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

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Responses

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent E Kovalick whose telephone number is 703 306-3020. The examiner can normally be reached on Monday-Thursday 7:30- 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703 305-4938. The fax phone numbers for the organization where this application or proceeding is assigned are 703 872-9314 for regular communications and 703 872-9314 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 306-0377.

Vincent E. Kovalick

Stovaliele

April 23, 2003

BIPIN SHALWALA

SUPERVISORY PATERIT I

TECHNOLOGY CONT